Docket No.: DATUMTE.018A   **Customer No. 20,995**

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Applicant   : Plasterer, et al.

App. No   : 10/820,381

Filed   : April 8, 2004

For   : SYSTEMS AND METHODS FOR
                 ACTIVELY PEAKED CURRENT-
                 MODE LOGIC

Examiner   : Anh Q. Tran

Art Unit   : 2819

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing nine (9) references to be considered by the Examiner. Also enclosed are two (2) non-patent literatures as listed on the Information Disclosure Statement. Identification herein is not an admission that any of the foregoing references are prior art to the above-captioned application.

This Information Disclosure Statement is being filed before the mailing date of a final action and before the mailing of a Notice of Allowance. This Statement is accompanied by the fees set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: January 3, 2006   By: _Michael Okamoto_

Michael S. Okamoto
Registration No. 47,831
Attorney of Record
Customer No. 20,995
(310) 551-3450

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INFORMATION DISCLOSURE		Application No.	10/820,381
		Filing Date	April 8, 2004
STATEMENT BY APPLICANT		First Named Inventor	Plasterer, et al.
		Art Unit	2819
(Multiple sheets used when necessary)		Examiner	Anh Q. Tran
SHEET 1 OF 1		Attorney Docket No.	DATUMTE.018A

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number *Number - Kind Code (if known)* Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
		3,633,120	01-04-1972	Battjes	
		5,689,178	11-18-1997	Otake	
		5,808,513	09-15-1998	Archer	
		5,889,425	03-30-1999	Kimura	
		6,084,472	07-04-2000	Gilbert	
		6,111,445	08-29-2000	Zerbe	
		6,433,595	08-13-2002	Tung	

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document *Country Code-Number-Kind Code* Example: JP 1234567 A1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T[1]

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[1]
		BARRIE GILBERT, *The Multi-tanh Principle: A Tutorial Overview*, IEEE Journal of Solid-State Circuits, Vol. 33, No. 1, January 1998, Institute of Electrical and Electronics Engineers, Inc., pp. 2-17.	
		BEHNAM AGHDAIE AND BING SHEU, *Overcoming Limitations of Lumped MOS Models*, Circuits & Devices, March 2000, pp 19-26	

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Examiner Signature	Date Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

T[1] - Place a check mark in this area when an English language Translation is attached.